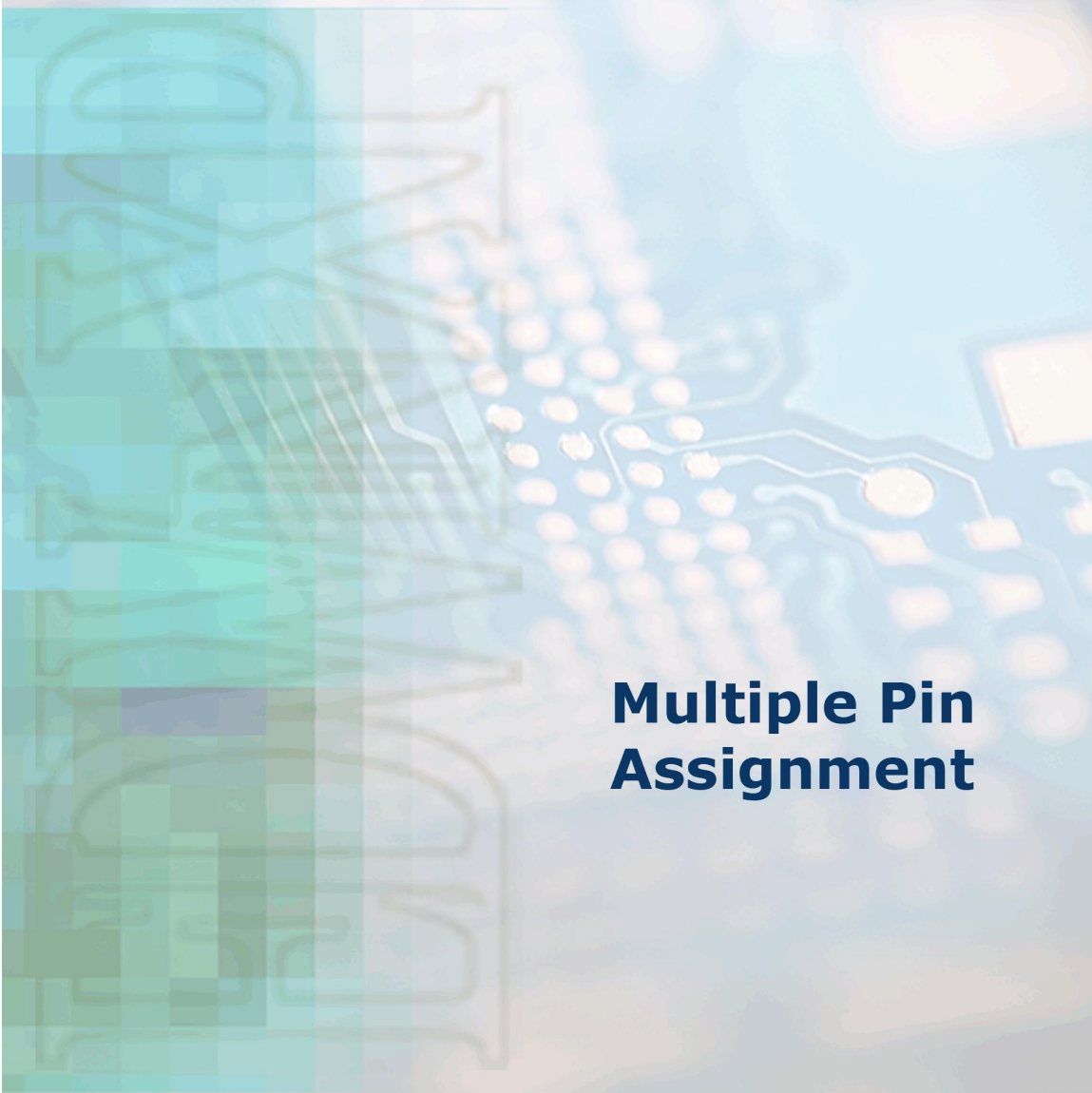


Electronic Design for Windows
EDWINXP



VISIONICS

© Norlinvest Ltd, BVI. Visionics is a trade name of Norlinvest Ltd. All Rights Reserved. No part of the Multiple Pin Assignment document can be reproduced in any form or by any means without the prior written permission of Visionics. Multiple Pin Assignment document is subjected to change without notice. Visionics will make changes in a manner that will not affect dependent systems.

Unauthorized duplication, in whole or part, of this document by any means, mechanical or electronic, including translation into another language, except for brief excerpts in published reviews, is prohibited without the express written permission of Visionics.

Visionics, EDWinXP, Docone, EDComX, SimWinXP and Mixed Mode Simulator and their respective logos are trademarks or registered trademarks of Visionics. Unauthorized duplication of this work may also be prohibited by local statute.

Disclaimer: Information in this publication is subject to change without notice and does not represent a commitment on the part of Visionics. The information contained herein is the proprietary and confidential information of Visionics or its licensors, and is supplied subject to, and may be used only by Visionics's customer in accordance with, a written agreement between Visionics and its customer. Except as may be explicitly set forth in such agreement, Visionics does not make, and expressly disclaims, any representations or warranties as to the completeness, accuracy or usefulness of the information contained in this document. Visionics does not warrant that use of such information will not infringe any third party rights, nor does Visionics assume any liability for damages or costs of any kind that may result from use of such information.

Contents

MULTIPLE PIN ASSIGNMENT.....4

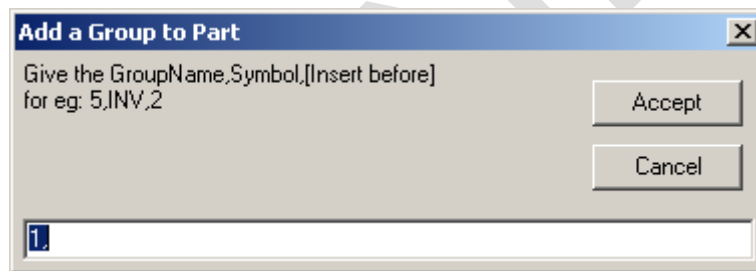
VISIONICS

Multiple Pin Assignment

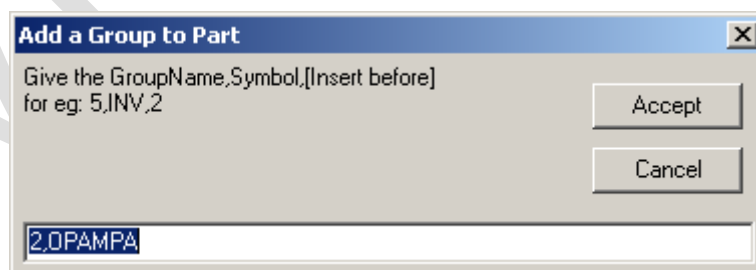
In the case of ICs, which comprises of more than one gate or having different symbols in a single component, multiple assignments make it possible to assign the different gates or symbols to a single part.

For eg: in the case of an OPAMP, the package is of a DIP 8/300. The symbol can be assigned by attaching its two groups to the part.

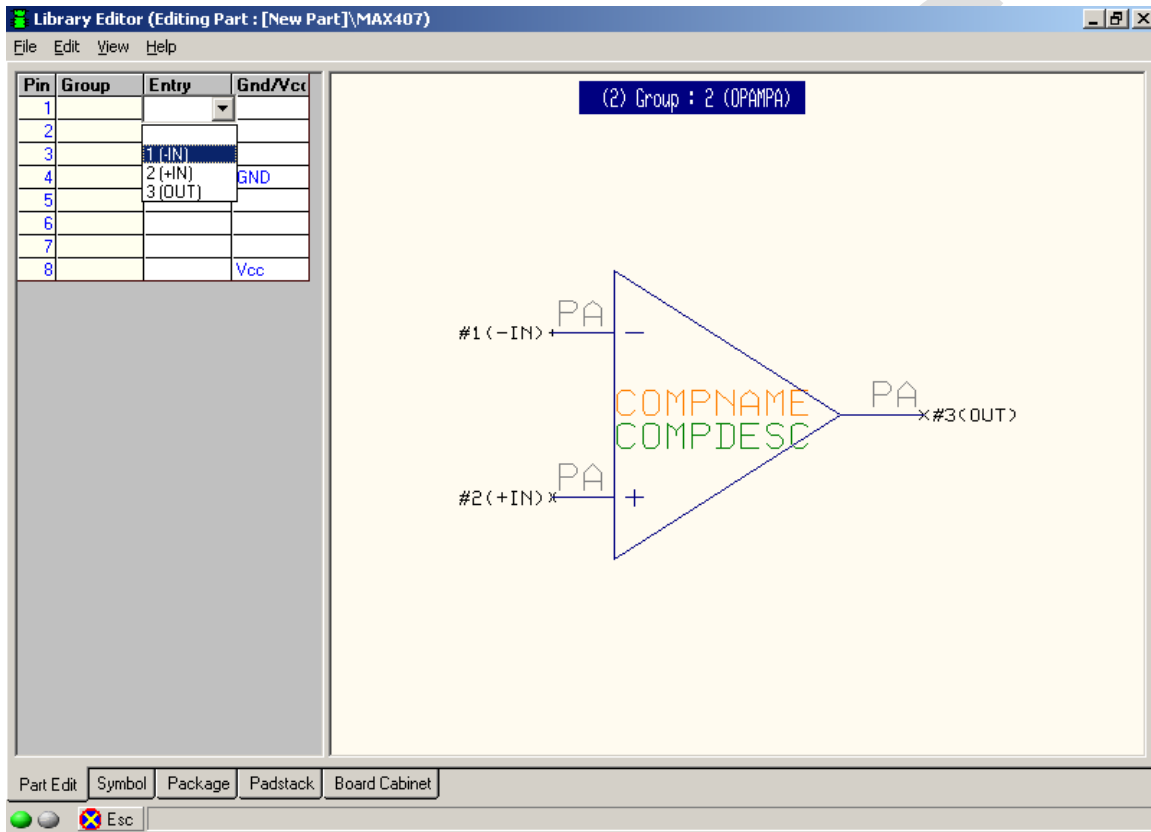
This can be accomplished from **Library Editor** → **Part edit** Tab Give the name of the package as DIP 8/300 in **Package details** → **Package and then Edit** → **Add Group**.



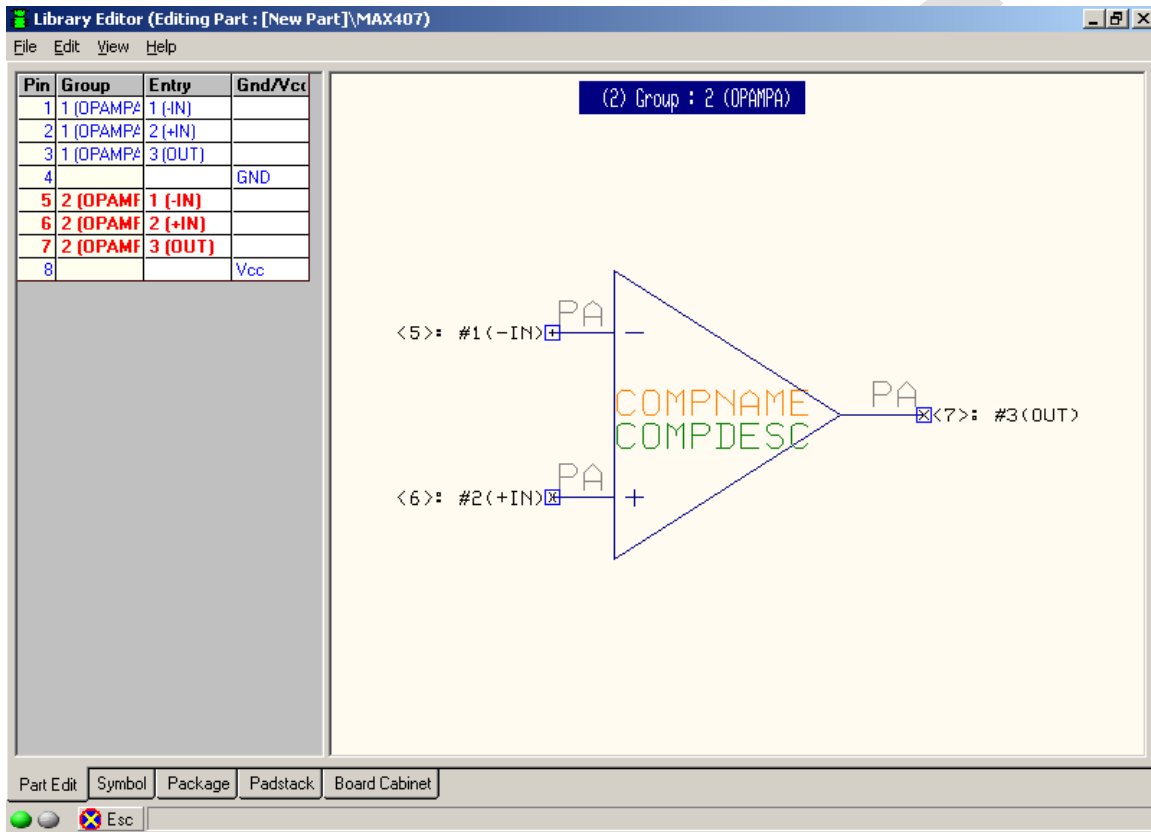
Enter the name of the symbol after the 1, to be included in the part. For adding another group, repeat the above process for Group 2.



For assigning pins to the package, go to **Edit → Edit Pinout Type** in as GND in the 4th pin and Vcc in the 8th pin. Then **Edit → First Group →** Select the drop down tab and add -IN, +IN and OUT for the 1, 2 and 3rd pins respectively. Again go to **Edit → Next Group**. Assign the pins as in the datasheet.



Finally all the pins can be assigned as below.



After assigning the pins, Select **File** → **Save Part**.