ELECTRONIC DESIGN for WINDOWS EDWINXP

List Generator

VISIONICS

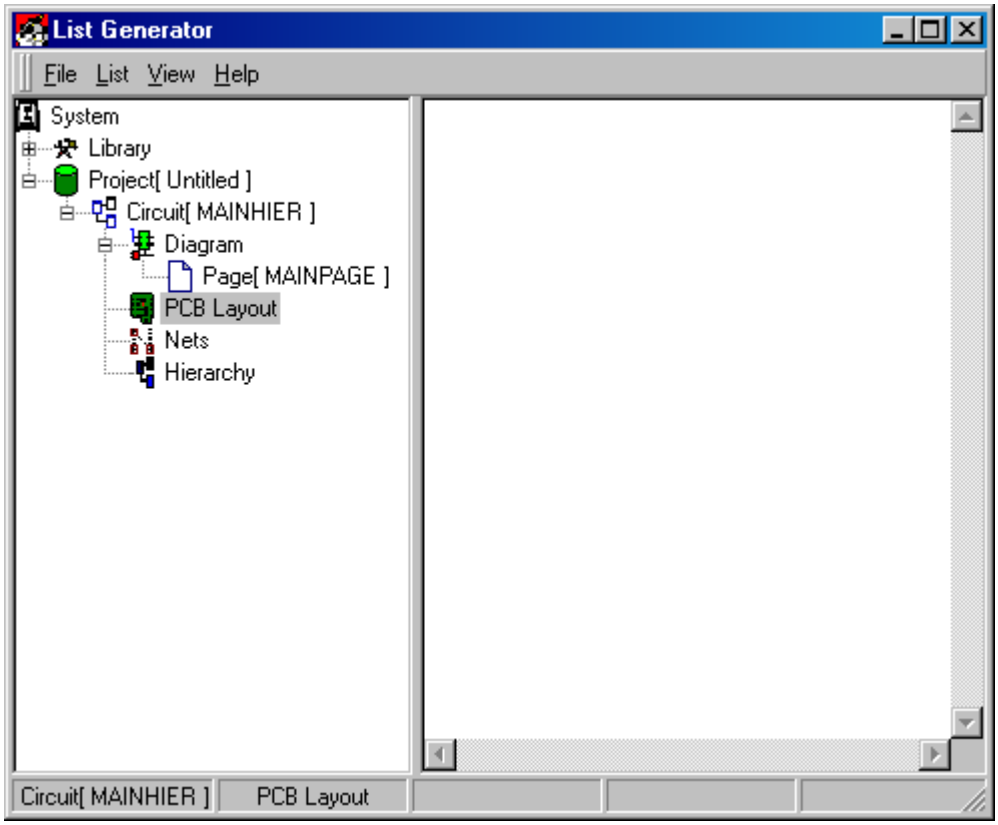
Contents

List Generator

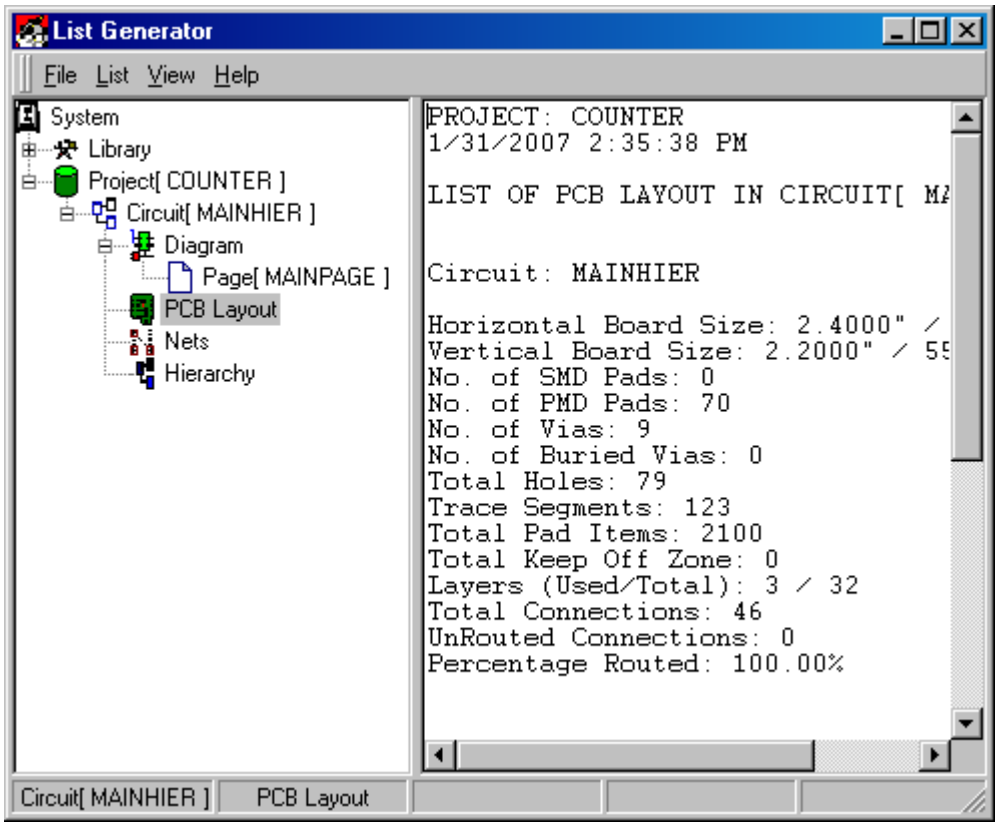
List Generator is a utility that allows generating a list containing requisite details of component library and Project. A general property description of the objects used in the project may be obtained for easy documentation and future verification. Details of objects specific to a particular circuit design; Layout, Nets, etc are given separately.

Invoking Library List generator

Invoke **List Generator** either from task list or task toolbar of the **System** in the project explorer, Library **List Generator** appears as shown below:



When a project is loaded, for eg, in the case of **Counter.epb**, the list generated is as shown below.



Horizontal Board Size: 2.4000" / 60.960mm

Vertical Board Size: 2.2000" / 55.880mm

No. of SMD Pads: 0

No. of PMD Pads: 70

No. of Vias: 9

No. of Buried Vias: 0

Total Holes: 79

Trace Segments: 123

Total Pad Items: 2100

Total Keep Off Zone: 0

Layers (Used/Total): 3 / 32

Total Connections: 46

UnRouted Connections: 0

Percentage Routed: 100.00%

LIST OF PARTS USED

1. 7404
2. 7486
3. 7420
4. 7474 (2 nos.)

LIST OF SYMBOL ONLY PARTS USED

1. SMB_VDC
2. SMB_SPL0
3. SMB_CLOCKA

Thus, it provides all the valid information's regarding a project, its nets, parts etc. It can be copied from the list generator