

Electronic Design for Windows  
**EDWINXP**



# List Generator

***VISIONICS***

© Norlinvest Ltd, BVI. Visionics is a trade name of Norlinvest Ltd. All Rights Reserved. No part of the List Generator document can be reproduced in any form or by any means without the prior written permission of Visionics. List Generator document is subjected to change without notice. Visionics will make changes in a manner that will not affect dependent systems.

Unauthorized duplication, in whole or part, of this document by any means, mechanical or electronic, including translation into another language, except for brief excerpts in published reviews, is prohibited without the express written permission of Visionics. Visionics, EDWinXP, Docone, EDComX, SimWinXP and Mixed Mode Simulator and their respective logos are trademarks or registered trademarks of Visionics. Unauthorized duplication of this work may also be prohibited by local statute.

Disclaimer: Information in this publication is subject to change without notice and does not represent a commitment on the part of Visionics. The information contained herein is the proprietary and confidential information of Visionics or its licensors, and is supplied subject to, and may be used only by Visionics's customer in accordance with, a written agreement between Visionics and its customer. Except as may be explicitly set forth in such agreement, Visionics does not make, and expressly disclaims, any representations or warranties as to the completeness, accuracy or usefulness of the information contained in this document. Visionics does not warrant that use of such information will not infringe any third party rights, nor does Visionics assume any liability for damages or costs of any kind that may result from use of such information.

## **Contents**

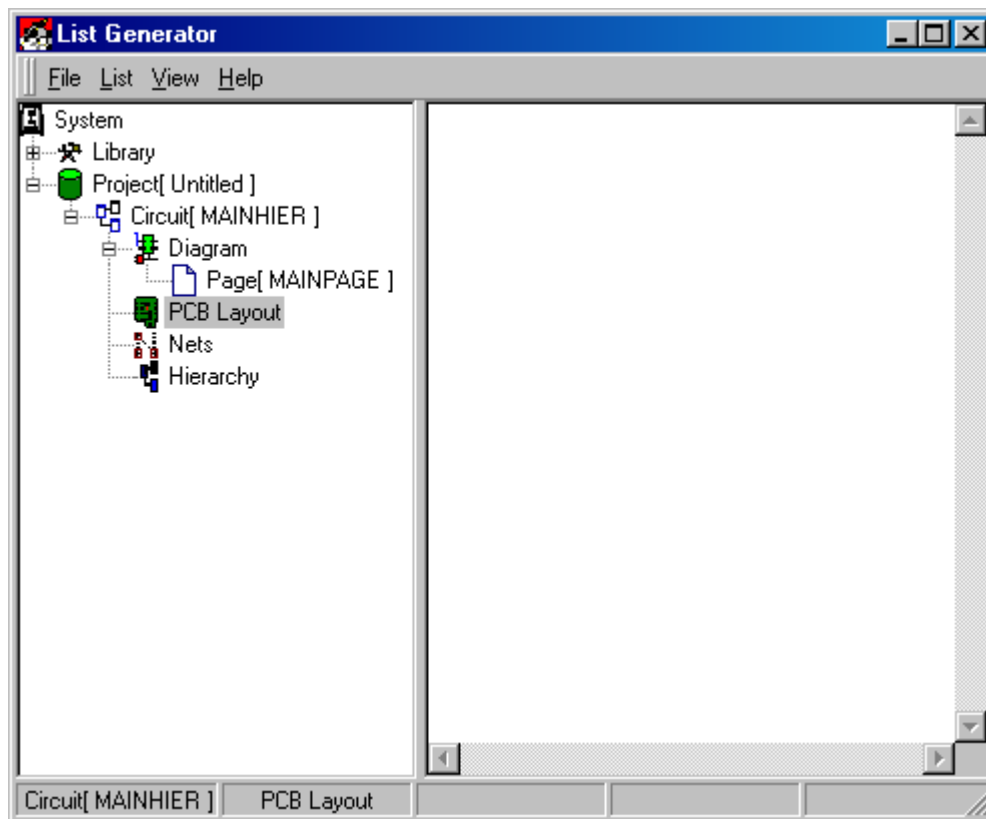
List Generator.....	4
Invoking Library List generator .....	4

## List Generator

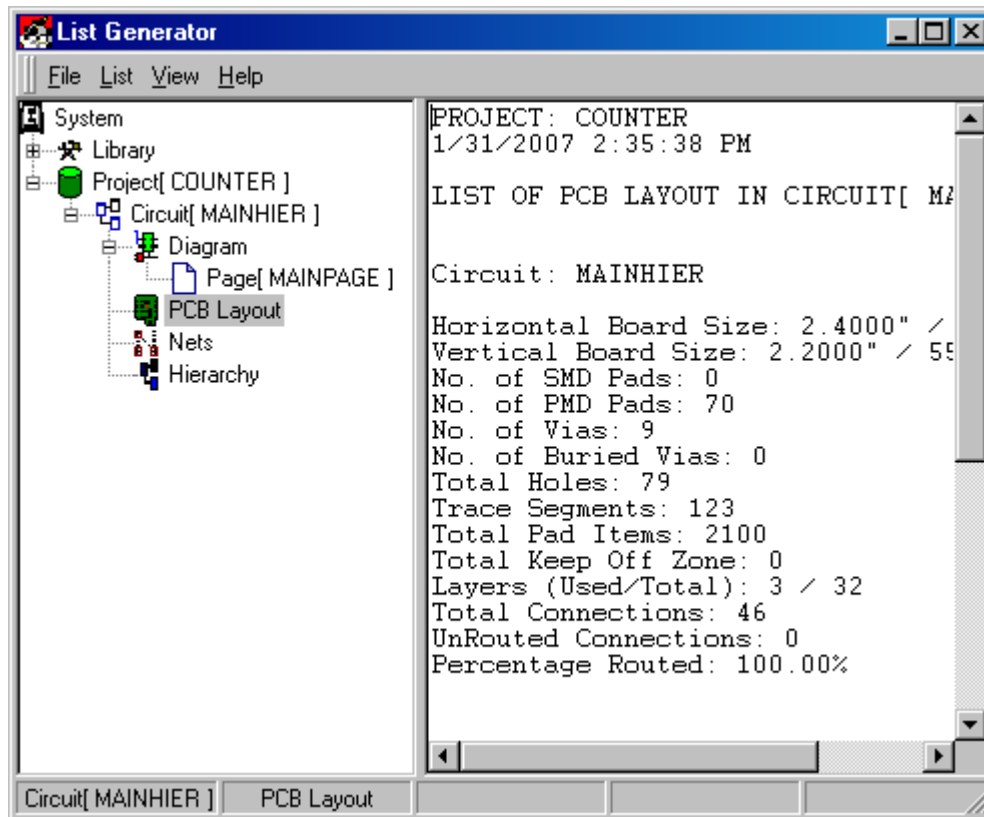
List Generator is a utility that allows generating a list containing requisite details of component library and Project. A general property description of the objects used in the project may be obtained for easy documentation and future verification. Details of objects specific to a particular circuit design; Layout, Nets, etc are given separately.

### Invoking Library List generator

Invoke **List Generator** either from task list or task toolbar of the **System** in the project explorer, Library **List Generator** appears as shown below:



When a project is loaded, for eg, in the case of **Counter.epb**, the list generated is as shown below.



Horizontal Board Size: 2.4000" / 60.960mm

Vertical Board Size: 2.2000" / 55.880mm

No. of SMD Pads: 0

No. of PMD Pads: 70

No. of Vias: 9

No. of Buried Vias: 0

Total Holes: 79

Trace Segments: 123

Total Pad Items: 2100

Total Keep Off Zone: 0

Layers (Used/Total): 3 / 32

Total Connections: 46

UnRouted Connections: 0

Percentage Routed: 100.00%

\*\*LIST OF PARTS USED\*\*

1. 7404
2. 7486
3. 7420
4. 7474 (2 nos.)

**\*\*LIST OF SYMBOL ONLY PARTS USED\*\***

1. SMB\_VDC
2. SMB\_SPL0
3. SMB\_CLOCKA

Thus, it provides all the valid information's regarding a project, its nets, parts etc. It can be copied from the list generator